

SEP 17 2002
O I P E J C 9 8
PATENT & TRADEMARK OFFICE
App

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:	Ha Z. KIM	Conf.:	2187
Appl. No.:	09/820,217	Group:	2822
Filed:	March 29, 2001	Examiner:	M. GUERRERO
For:	SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME		

LARGE ENTITY TRANSMITTAL FORM

September 17, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	11	-	20	=	0	\$18	\$0.00
INDEPENDENT	2	-	3	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By *James T. Ellis, Jr.* #39,538
Joseph A. Kolasch #22,463

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ATTACHMENT

(Rev. 09/27/01)



#10/Amatt B
PATENT
0465-0751E
9/20/02

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SEP 19 2002
TECHNOLOGY CENTER 2800

AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

September 17, 2002

Sir:

In response to the Examiner's Office Action dated June 17, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

In the Claims:

Please cancel claim 9 without prejudice or disclaimer of the subject matter contained therein.

Please amend the claims as follows:

5. (Amended) A method for manufacturing a semiconductor device comprising:

preparing a semiconductor substrate of a first conductivity type;

See
C1
B1